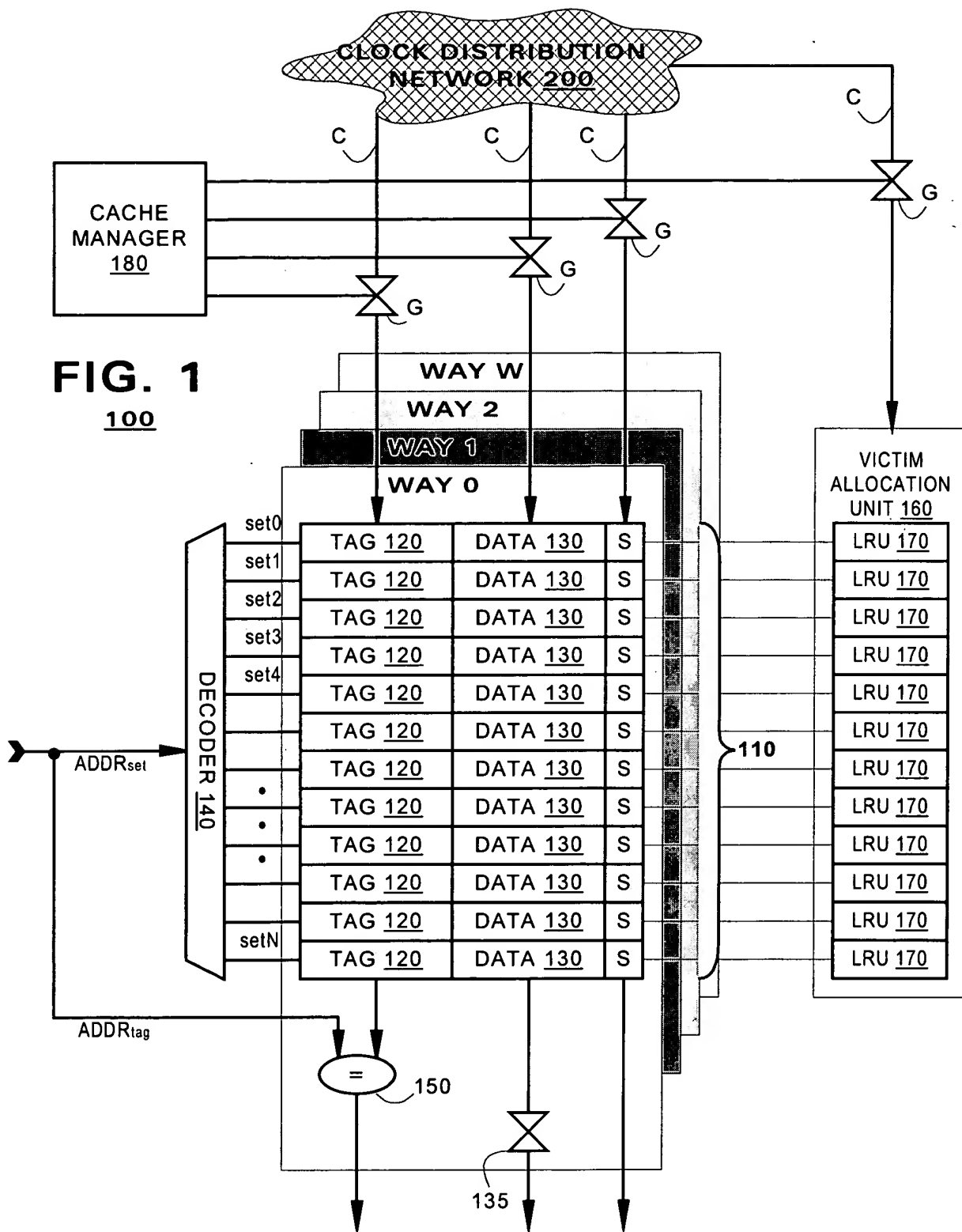
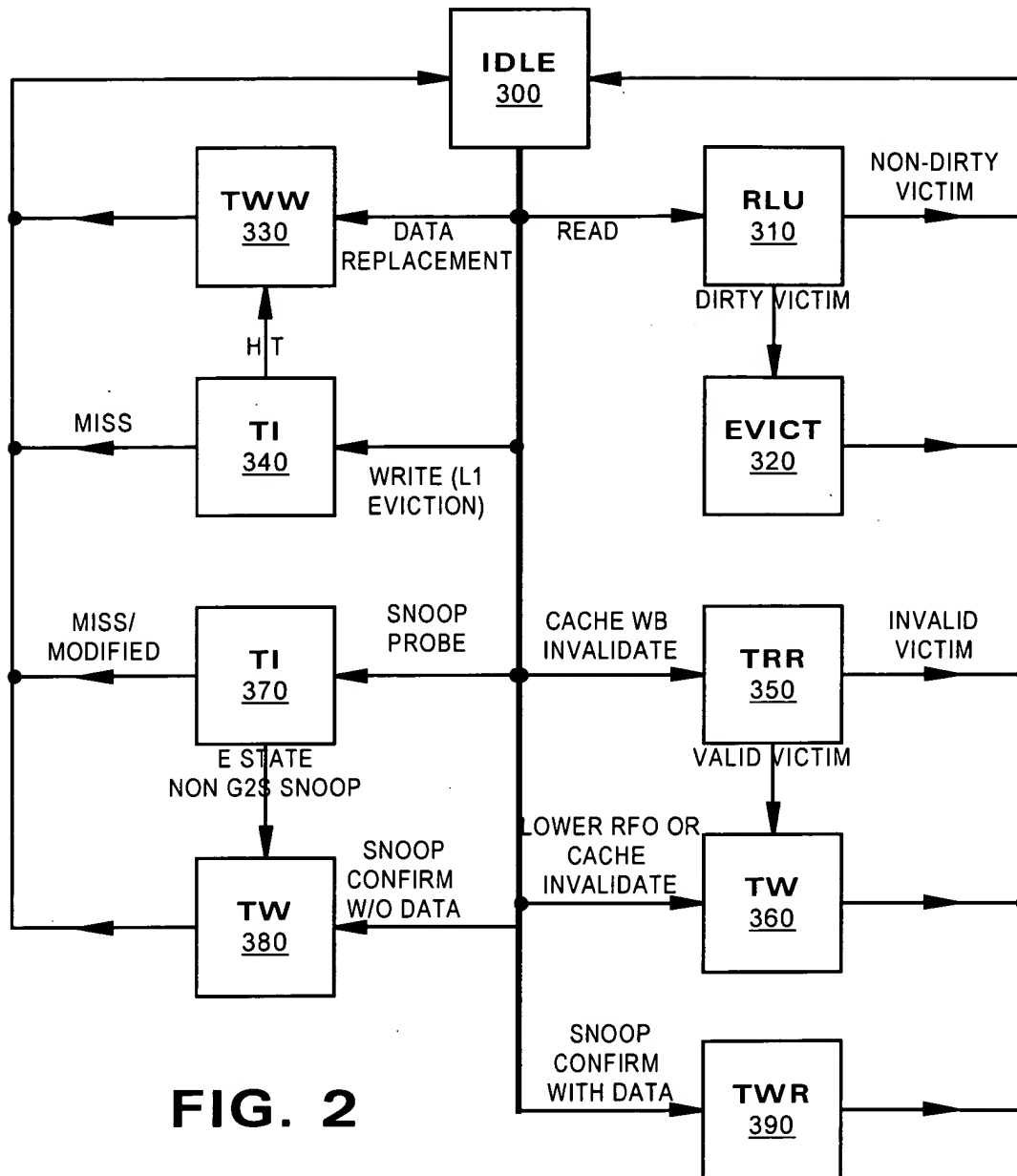
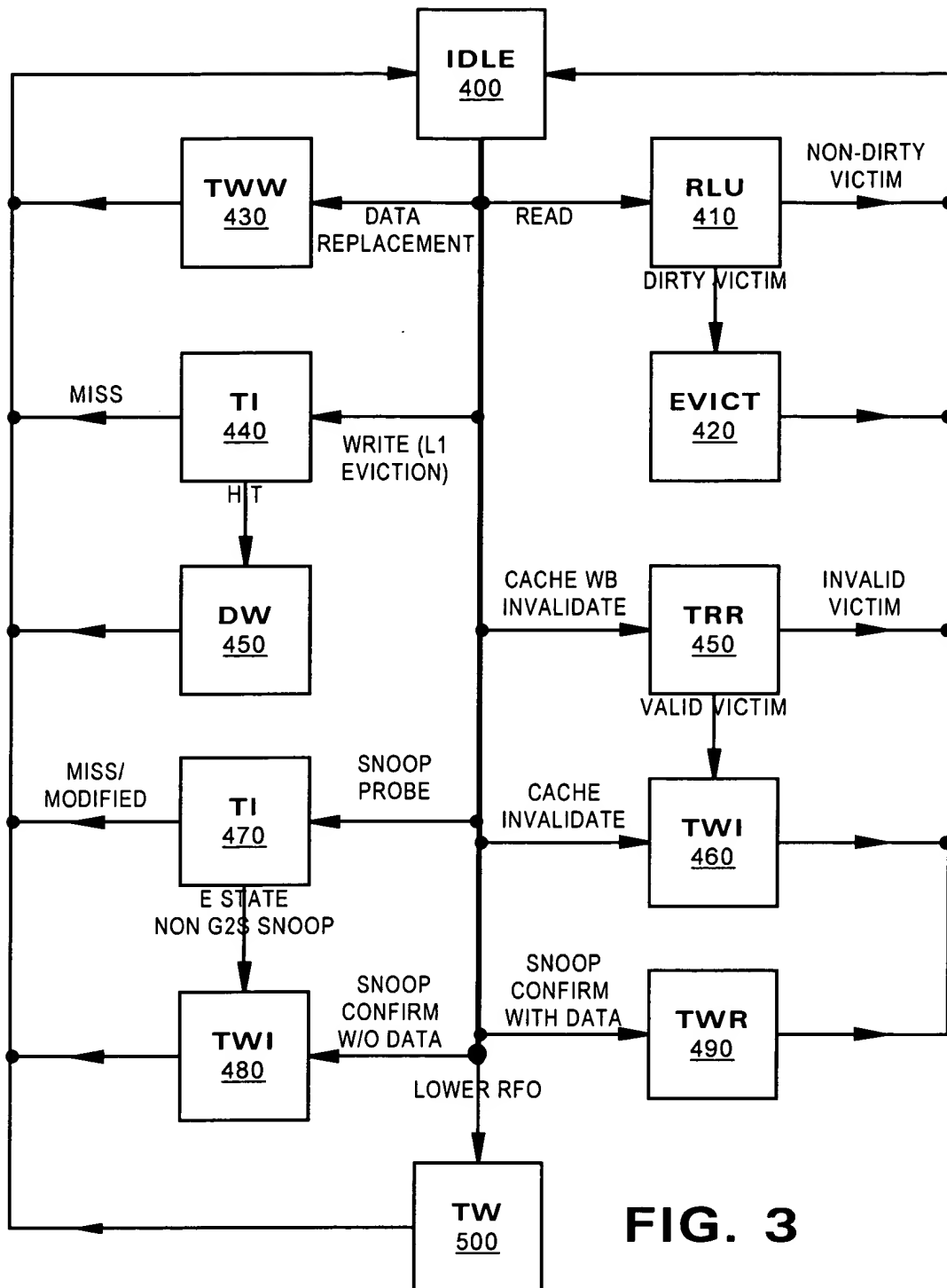


09749750.122800







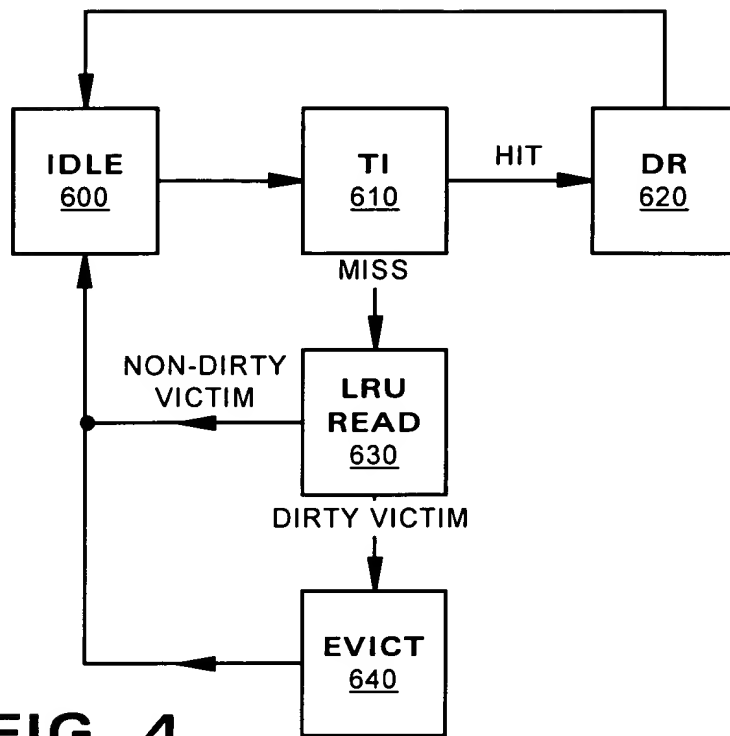


FIG. 4

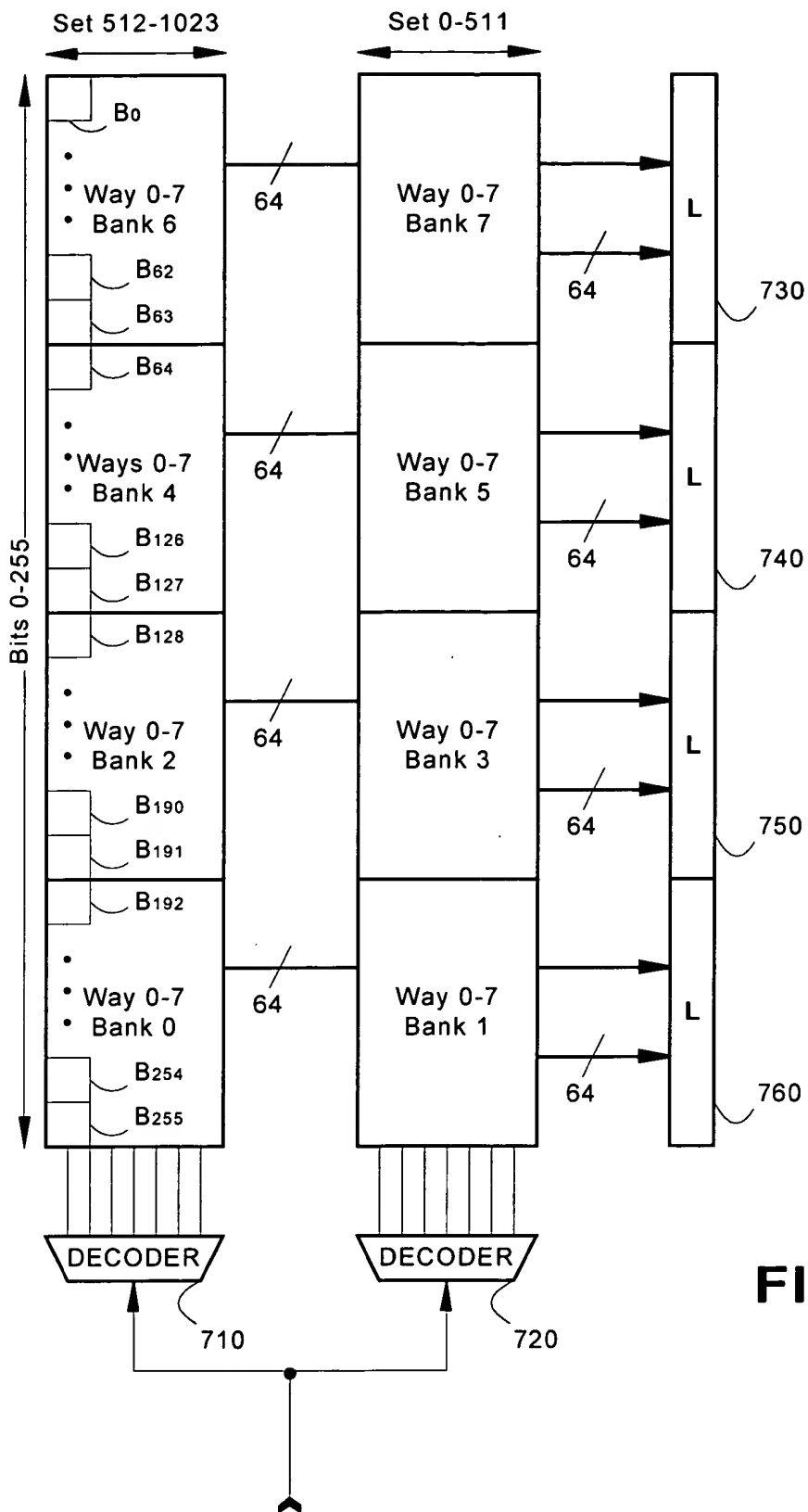


FIG. 5
700

Figure 1 is a block diagram of a 4-way set associative cache architecture. The diagram is organized into a 4x4 grid of cache blocks. The columns are labeled "Set 968-1023", "Set 512-967", "Set 256-511", and "Set 0-255". The rows are labeled "Way 6-7 Bank 15", "Way 4-5 Bank 14", "Way 2-3 Bank 13", and "Way 0-1 Bank 12". Each block contains a "Way" and a "Bank" number. The blocks are connected to a common "BANK SELECT" line. The output of each block is connected to a common "L" line. The diagram also shows a "BANK SELECT" line and a "BANK SELECT" line. The diagram is labeled "FIG. 1".